



FIG. 1

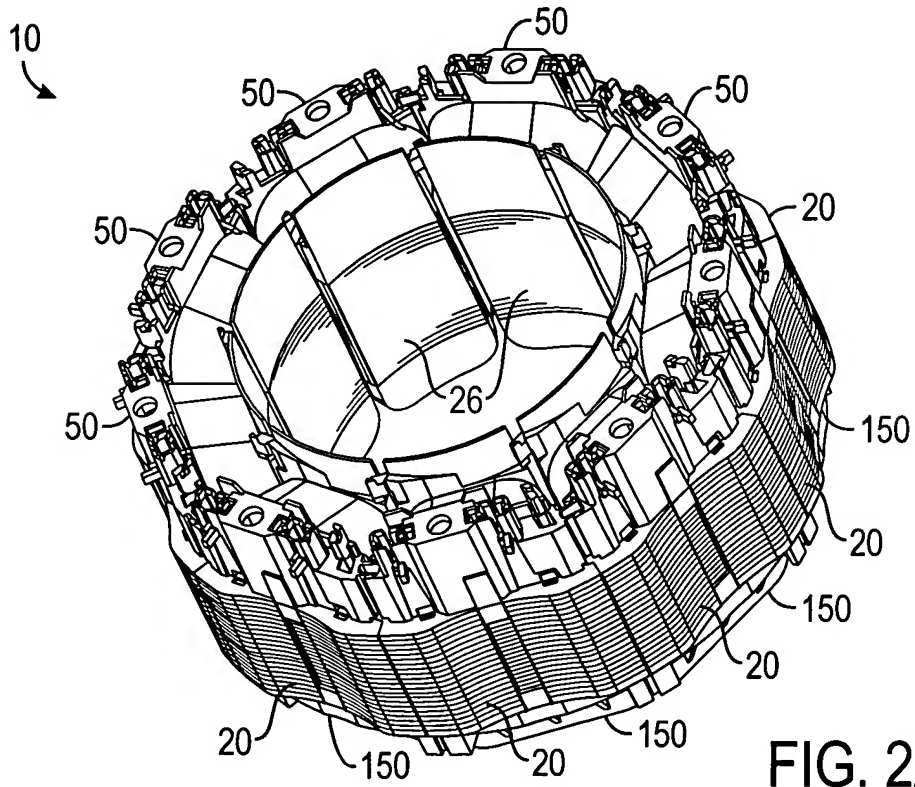


FIG. 2A

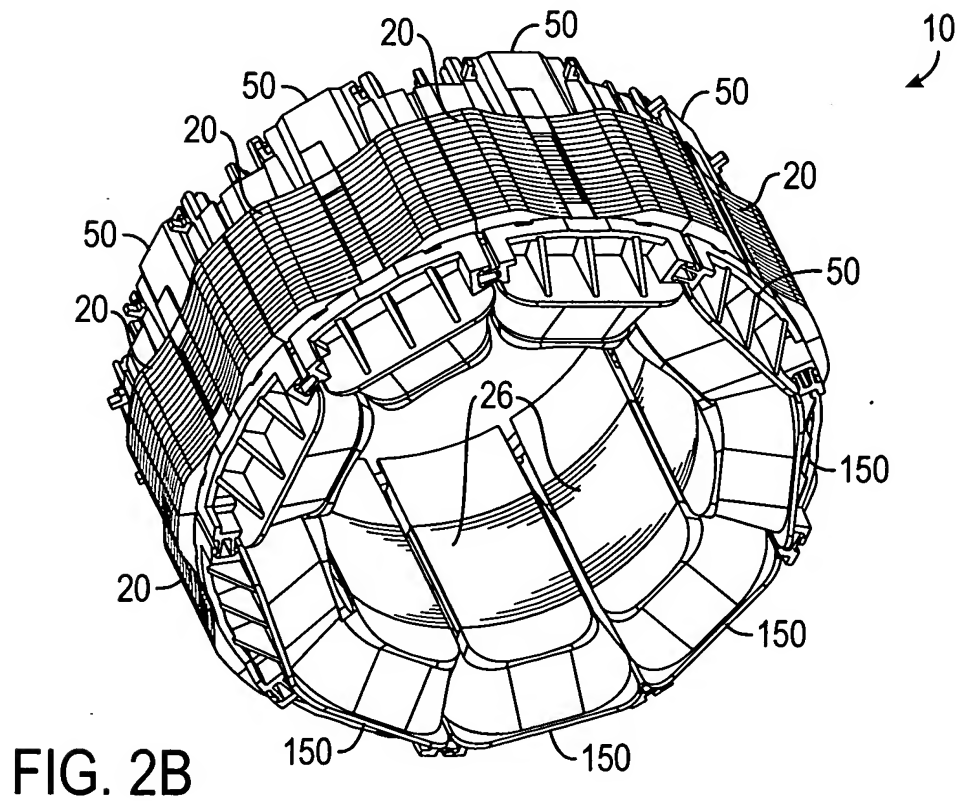
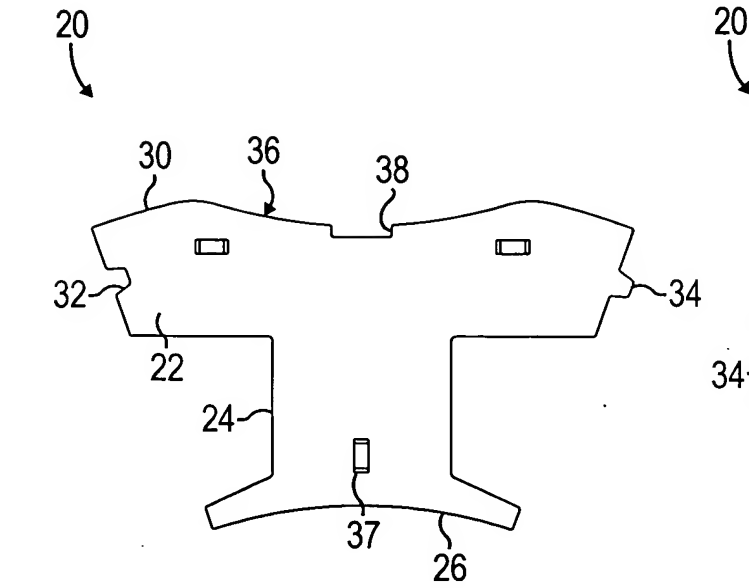
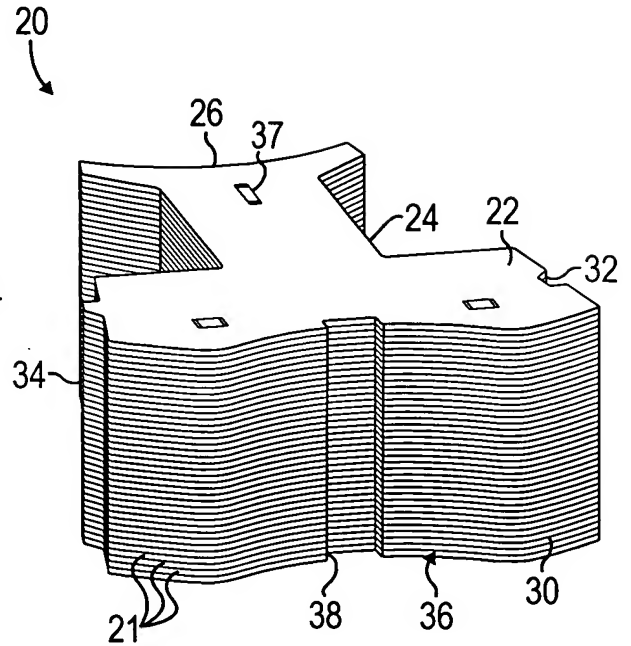


FIG. 2B





**FIG. 3A**



**FIG. 3B**

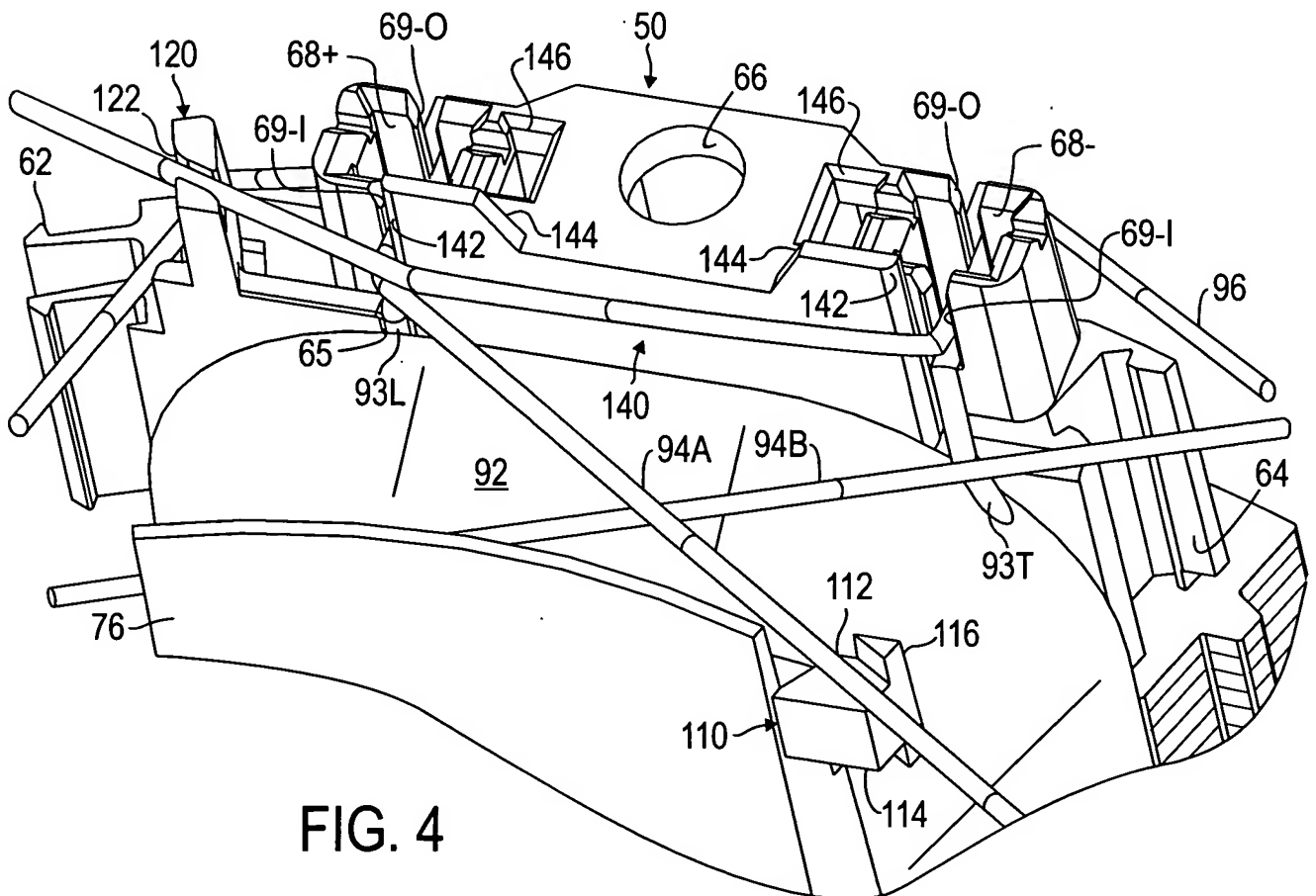


FIG. 4

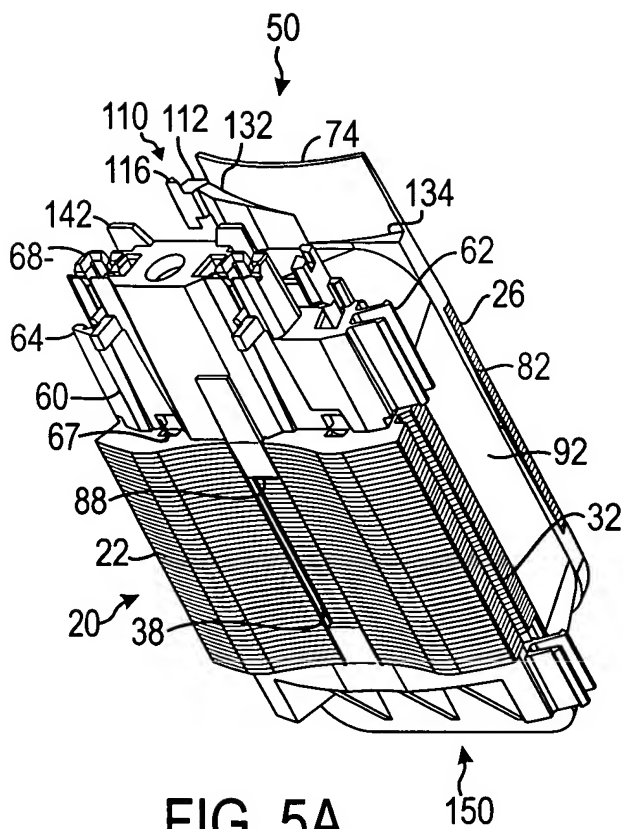
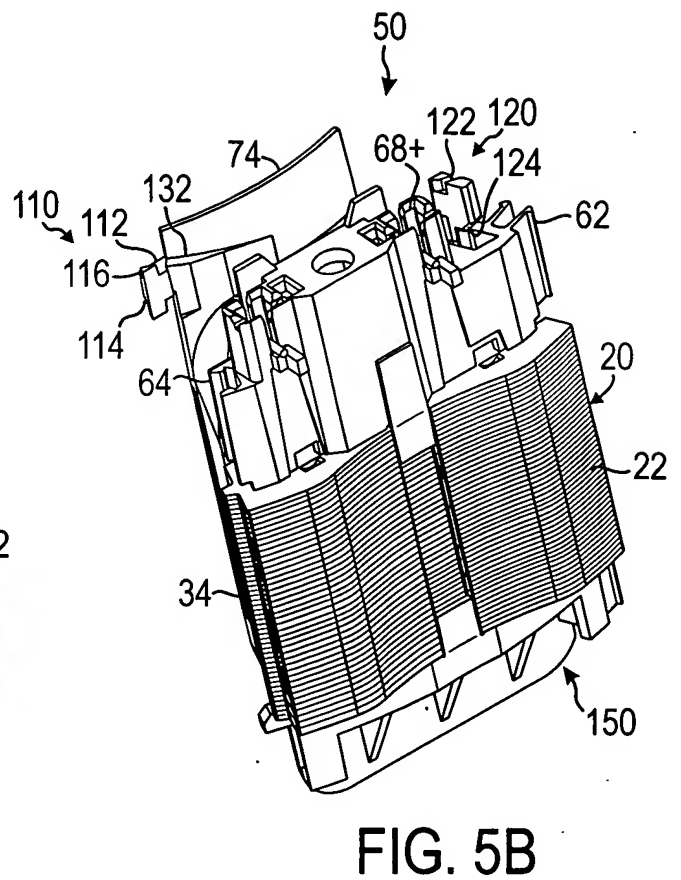


FIG. 5A



**FIG. 5B**

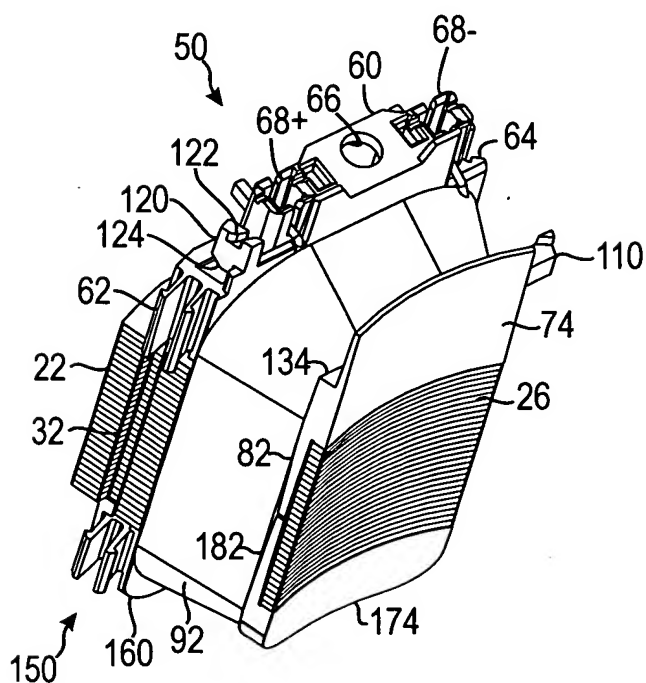


FIG. 5C

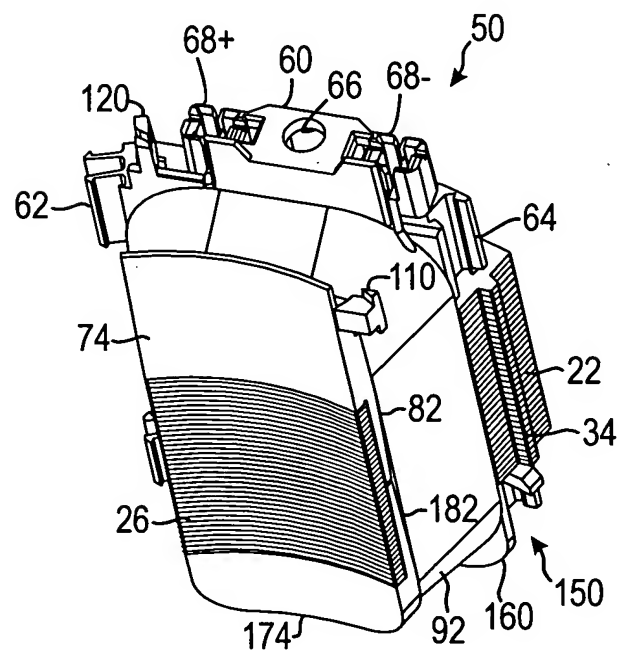


FIG. 5D

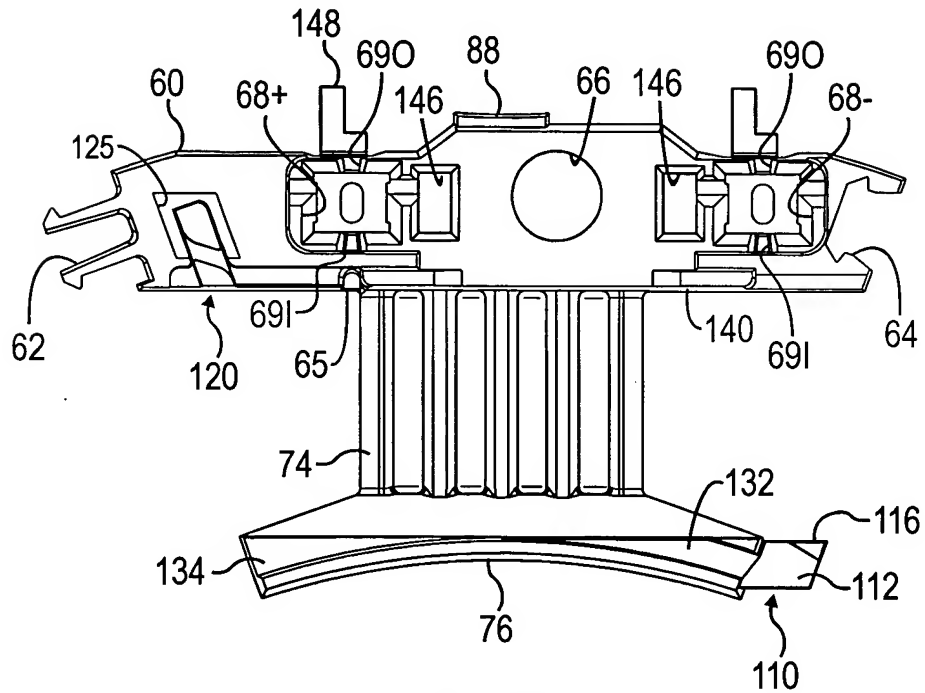


FIG. 6A

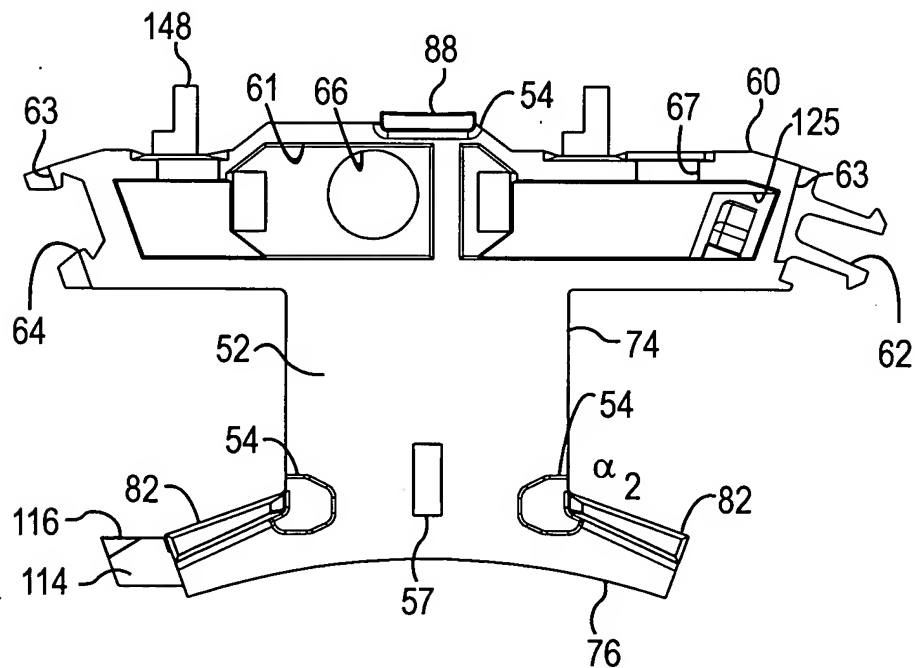


FIG. 6B

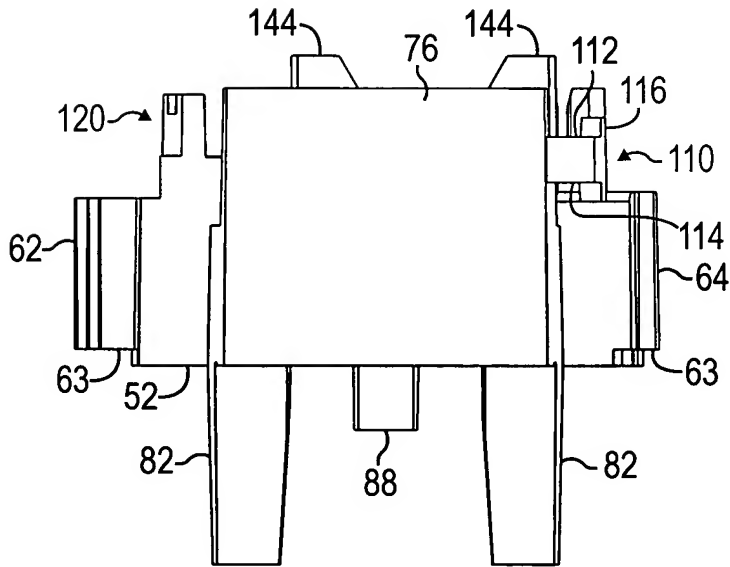


FIG. 6C

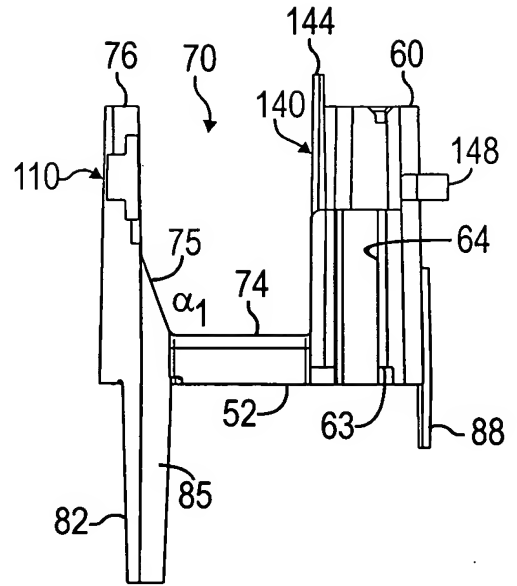


FIG. 6D

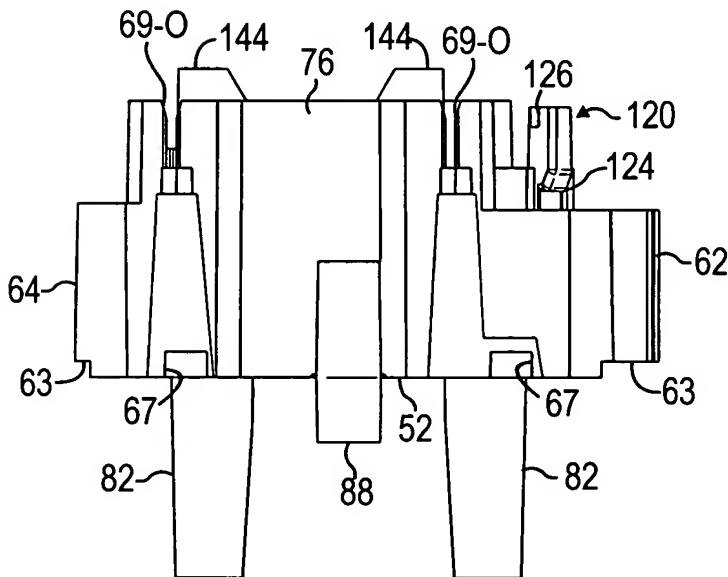


FIG. 6E

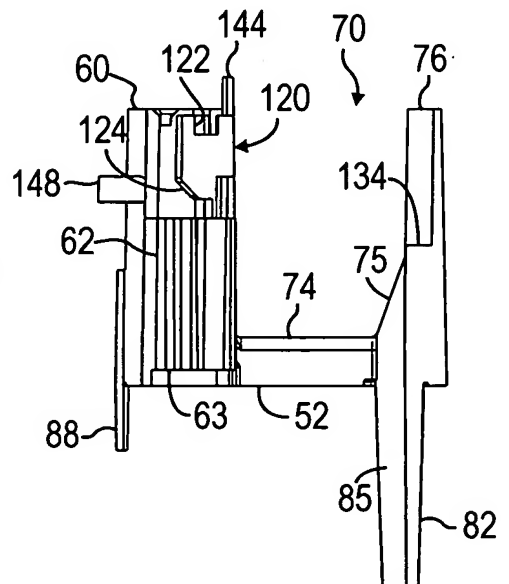


FIG. 6F



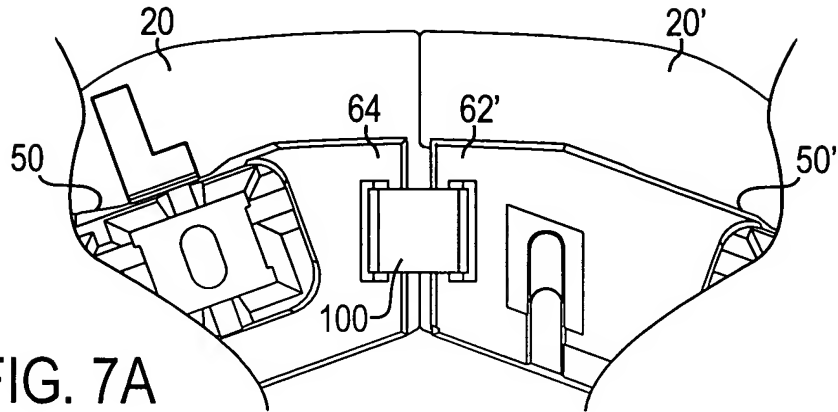
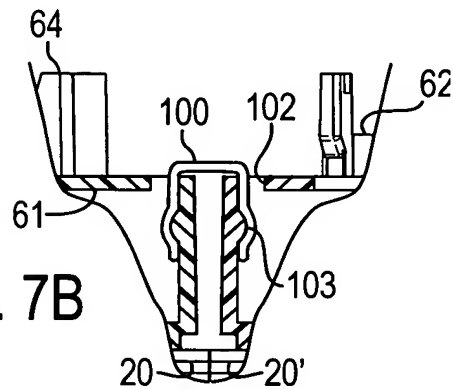
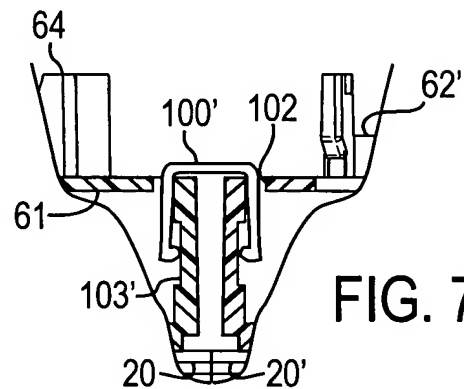


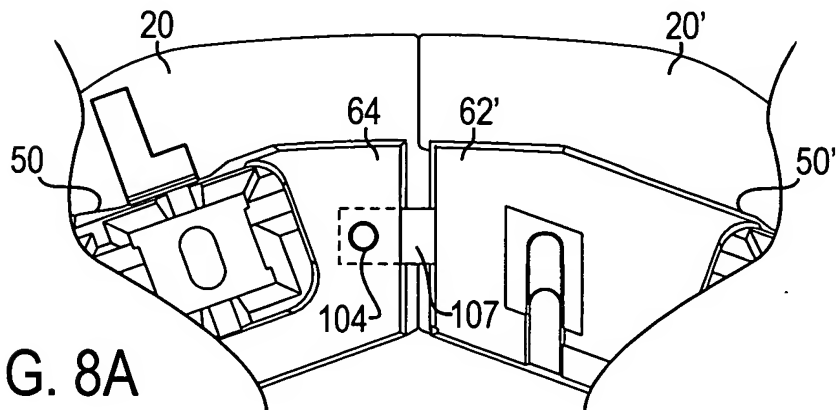
FIG. 7A



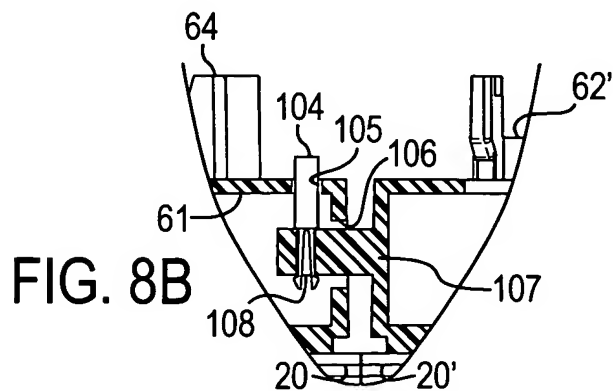
**FIG. 7B**



**FIG. 7C**



**FIG. 8A**



**FIG. 8B**





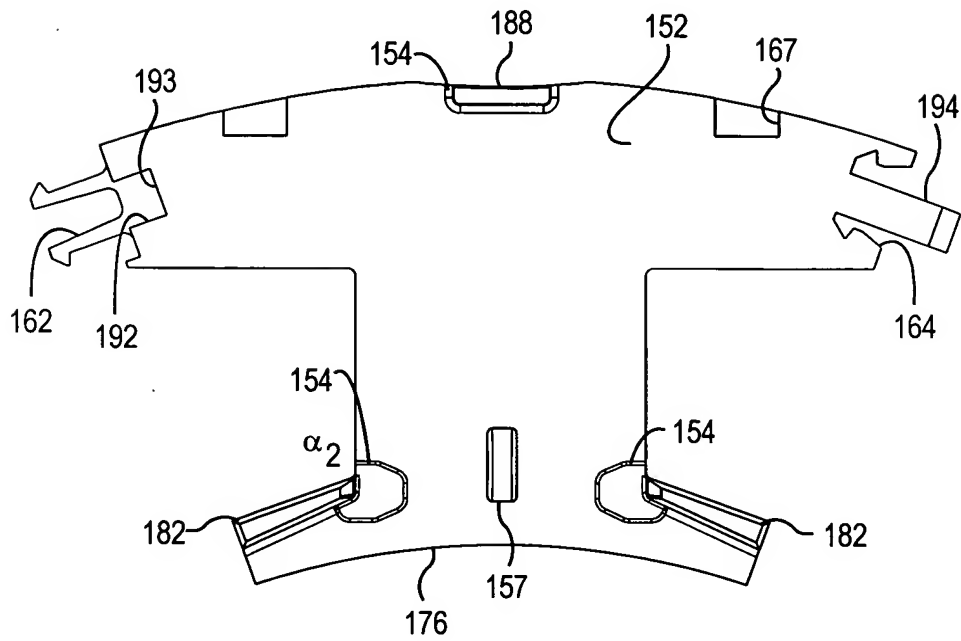


FIG. 10A

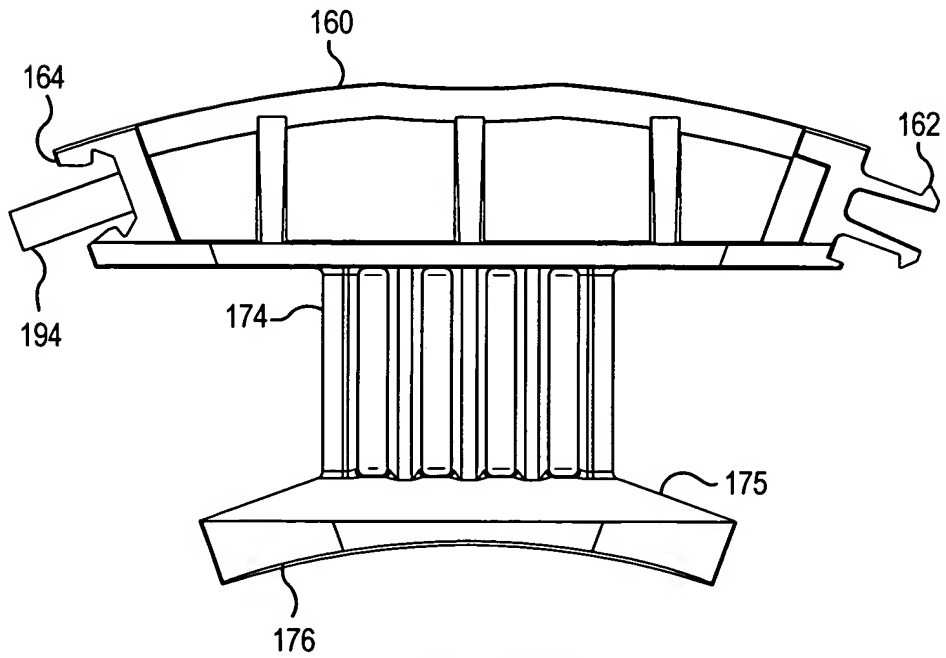


FIG. 10B



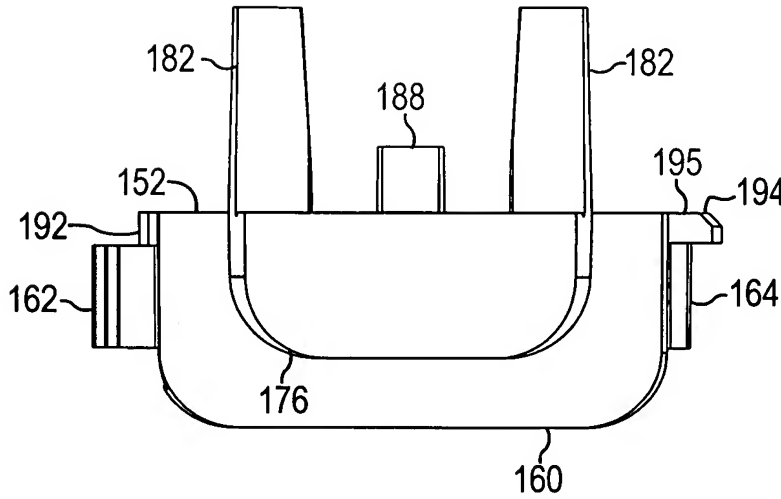


FIG. 10C

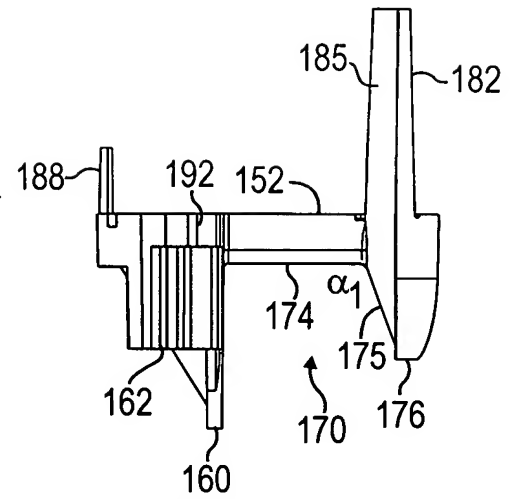


FIG. 10D

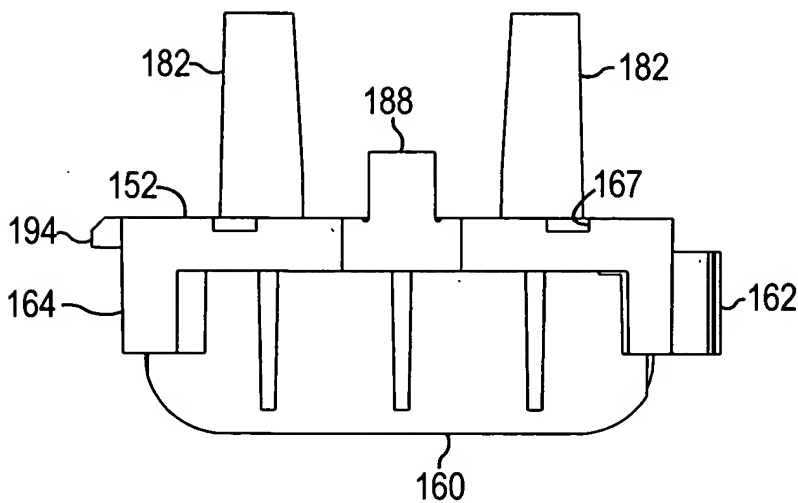


FIG. 10E

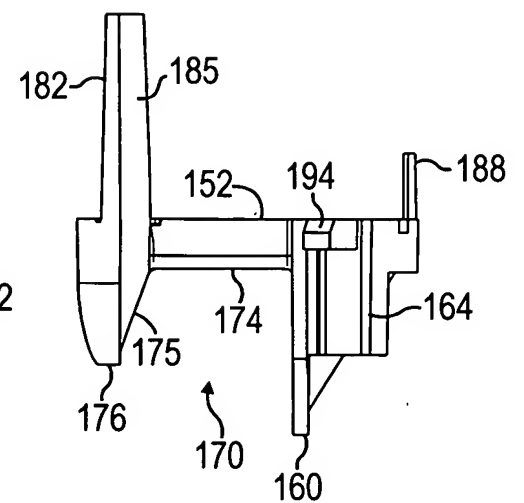


FIG. 10F



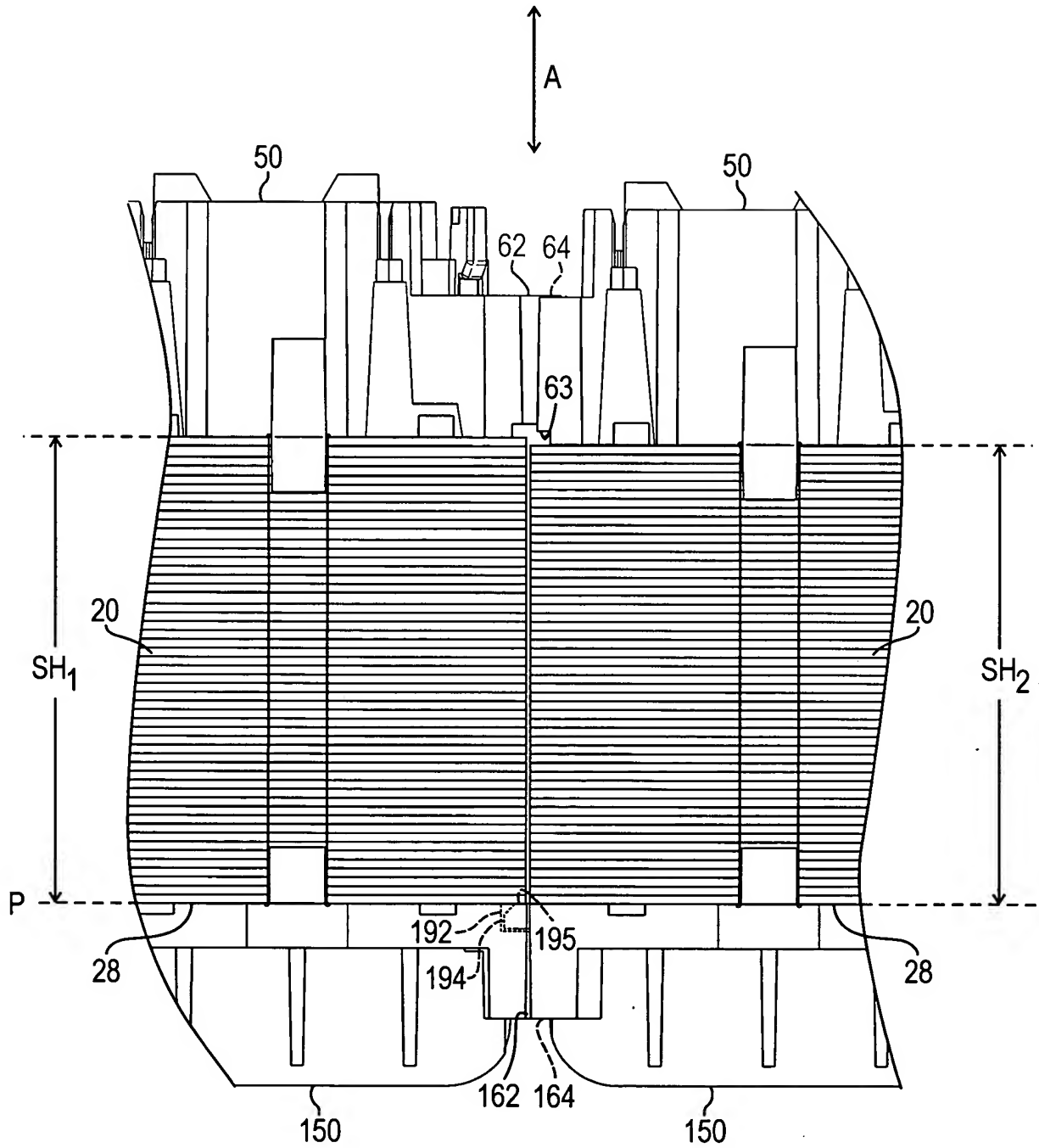


FIG. 11

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### Phase-C Interconnect

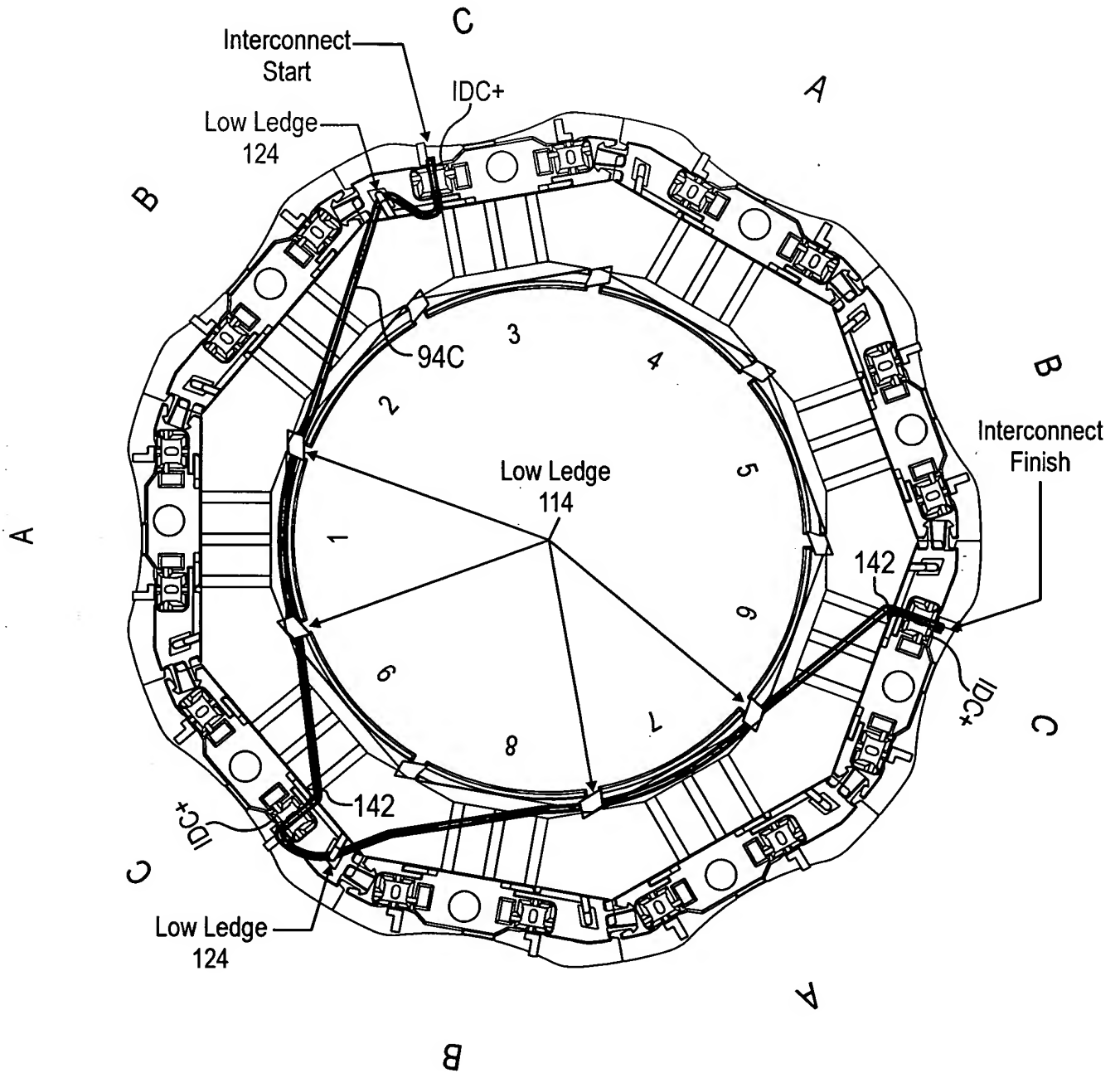


FIG. 12A

⊕

## C



## C



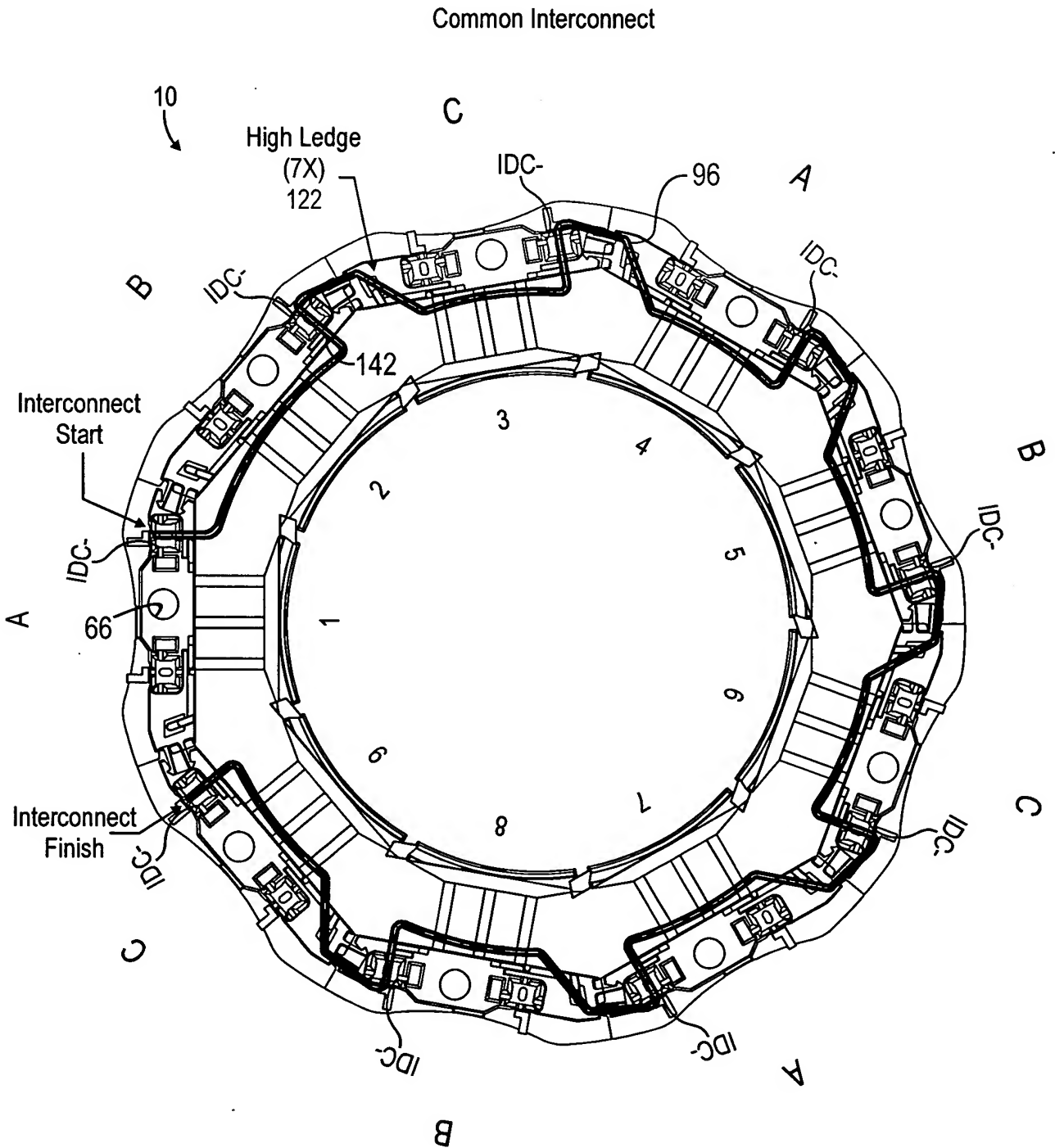


FIG. 12D

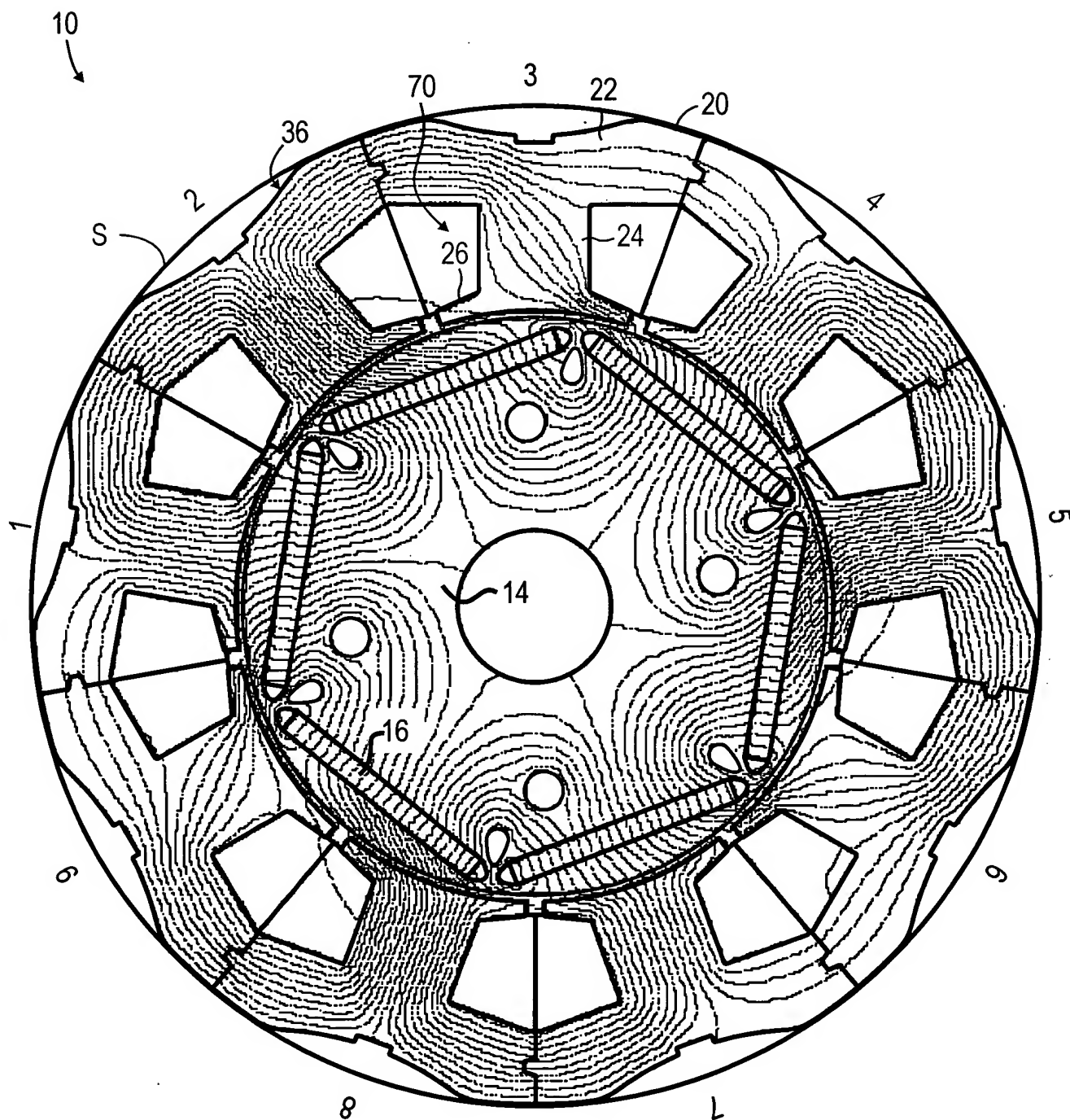


FIG. 13



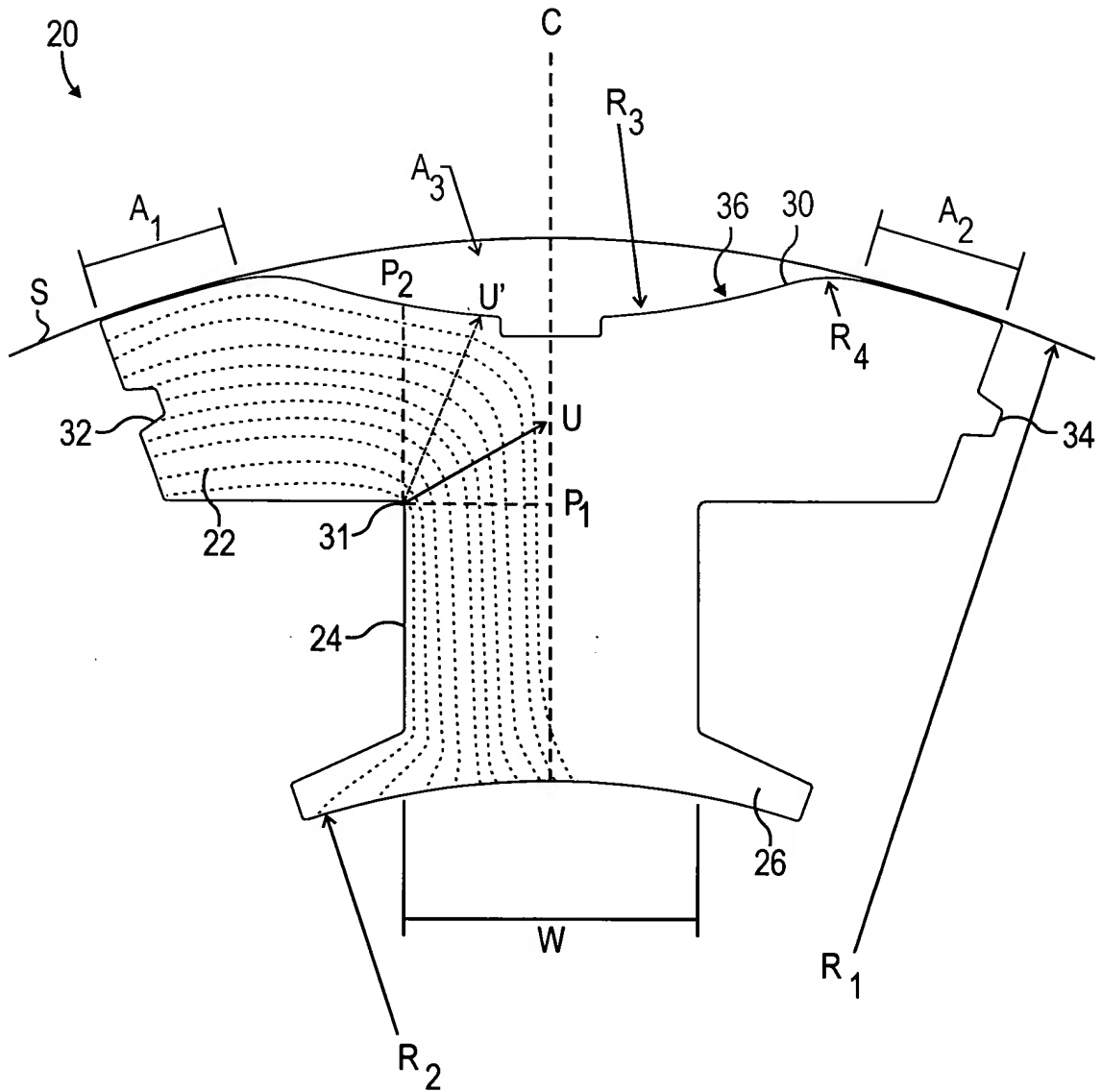


FIG. 14